



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Diploma (Electrical Engineering)
Choice Based Credit System (CBCS)

SEMESTER - II (w.e.f. Batch 2019-20)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	DTMA201	Applied Mathematics-II	3	1	0	4	60	20	20	0	0	100
2	DTCE101	Applied Mechanics	2	1	2	4	60	20	20	30	20	150
3	DTEE201	Electrical Circuits	2	1	2	4	60	20	20	30	20	150
4	DTHU101	Communication Skills	0	0	4	2	0	0	0	30	20	50
5	DTME102	Basic Mechanical Engineering	3	0	2	4	60	20	20	30	20	150
6	DTEE202	Basic Electronics Engineering	2	1	2	4	60	20	20	30	20	150
			12	4	12	22	300	100	100	150	100	750

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Registrar

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore